



## Basic PLC Programming

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Table CPU Capabilities

14-Point Micro PLCs	23 and 28-Point Micro PLCs
H8/3003 microprocessor running at 9.84Mhz Powerup reset circuit Interrupt for power fail warning (2.0 ms) Internal Coils - 1024 Four configurable 5Khz HSCs	
512K x 8 sectored flash memory for operating system and nonvolatile user program storage (3K words of user flash memory)	256K x 16 sectored flash memory for operating system and nonvolatile user program storage (6K words of user flash memory)
32 Kbyte RAM backed by super cap (provides data retention for 3-4 days with the power off at 25°C)	64 Kbyte RAM backed by lithium battery Real time clock backed up by lithium battery
Maximum User Program - 3K words	Maximum User Program - 6K words
Registers - 256 words	Registers - 2K words
Typical Scan Rate: 1.8 ms/K of logic (Boolean contacts)	Typical Scan Rate: 1.0 ms/K of logic (Boolean contacts)
An RS-422 serial port that supports SNP, SNPX and RTU Slave protocols	Two RS-422 serial ports: Port 1 supports SNP/SNPX slave protocols; Port 2 supports SNP/SNPX Slave and Master protocols and RTU Slave protocol. (Port 2 does not support the HHP.)
	Ability to support up to four expansion units

### High Speed Counters (IC693UDR011/002/005, IC693UAL006, IC693UDR010)

The high speed counter (HSC) function consists of four built-in counters. Each counter provides direct processing of rapid pulse signals up to 5Khz for industrial control applications such as: meter proving, turbine flow meter, velocity measurement, material handling, motion control, and process control. Because it uses direct processing, the HSC can sense inputs, count, and respond with outputs without needing to communicate with the CPU.

The HSC function can be configured to operate in one of two modes:

**A4** – four identical, independent, simple (type A) counters that can count up or down

**B1-3, A4** – counters 1-3 configured as one type B counter; counter 4 as one type A counter.

In either mode, each counter can be enabled independently. Type A counters can be configured for up or down counting (default is up) and for positive or negative edge detection (default is positive). The HSC function is configured using the Series 90-30 and 90-20 Hand-Held

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RFI Standards GFK-1065F iii The Series 90 Micro PLCs have been tested and found to meet or exceed the requirements of FCC Rule, Part 15, Subpart J.

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